

PATENT NUMBER

U.S. UTILITY Patent Application

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| IP/E | PATENT DATE |
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| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
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| 09/713036 | F | 216 | 13 | 1746 | Ahmed |

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APPLICANTS

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TITLE

Method of manufacturing multi layer printed wiring board

COPY

PTO-20
12/89

[illegible]

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|--|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) _____ (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| | | | | | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) _____ (Date) | | | ISSUE FEE | |
| | | | | Amount Due _____ Date Paid _____ | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) _____ (Date) | | | ISSUE BATCH NUMBER | |
| | | | | | |
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